

## SV92PP

## PCI Soft Modem



## FEATURES

- Two-chip PCI soft modem solution with an integrated 5 V tolerant interface that supports the PCI revision 2.3 standard:
  - SV92PP host interface in a 68-pin MLCC.
  - CSP1040 DAA in a 20-pin TSSOP.
- Data mode capabilities:
  - ITU-T™ V.92\*: 56000 bits/s—28000 bits/s.
  - ITU-T V.90\*: 56000 bits/s—28000 bits/s.
  - ITU-T V.34: 33600 bits/s—2400 bits/s.
  - V.32bis and fallbacks.
  - V.44, V.42, V.42 bis, and MNP™ Class 5 data compression.
  - High compression throughput due to parallel access directly to the host PC.
- FAX mode capabilities:
  - ITU-T T.31 class 1 FAX.
  - ITU-T V.17: 14400 bits/s, 12000 bits/s, 9600 bits/s, 7200 bits/s (TCM).
  - ITU-T V.29: 9600 bits/s, 7200 bits/s (QAM).
  - ITU-T V.27ter: 4800 bits/s, 2400 bits/s (DPSK).
  - ITU-T V.21 Channel 2: 300 bits/s (FSK).
- CSP1040:
  - System-powered.
  - Proprietary isolation barrier.
  - Programmable event detect for caller-ID reception and power ring detection.
  - Programmable pulse shaping and spark quench.
  - Programmable dc-impedance termination for country-specific VI templates.
  - Programmable ac-impedance termination for return-loss matching.
  - Programmable ringer-impedance emulation.
- Hardware support for pulse dialing for accurate make/break timing.
- Wake-on-ring and caller ID support.
- Common driver across multiple platforms.



The SV92PP chip set using LSI's third-generation silicon DAA is a soft modem solution for PCI applications. The chip set works with LSI's standard soft modem drivers and supports V.92 and lower rates.

The CSP1040 device is LSI's third-generation silicon DAA, which reduces the number of components and board area required to implement a full-featured modem, while achieving compliance with worldwide regulatory requirements. A low-profile digital transformer provides the communications link between the CSP1040 and SV92PP devices. This digital link also provides power to the CSP1040, allowing full operation on marginal phone lines.

The CSP1040 includes hardware support for detecting line-in-use status, overcurrent, polarity reversals, caller ID, and ringing, without the need for additional external circuitry. This allows for full-featured modem designs without increased bill-of-materials or board space.

## OPERATING SYSTEM SUPPORT

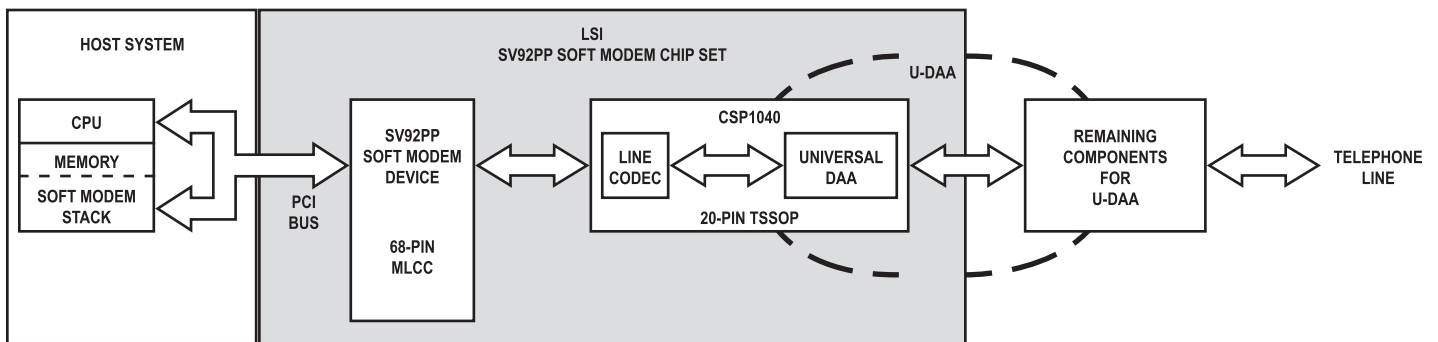
- Windows® 98, Windows 2000, Windows ME®, and Windows XP 32/64-bit Editions, Windows VISTA 32/64-bit Editions
- Microsoft® Designed for Windows logo device requirement compliant

\* Actual speeds over U.S. telephone lines vary and are less than 56K due to current FCC regulations and line conditions.

## Functional Description

The LSI SV92PP chip set is a two-chip solution consisting of an LSI SV92PP PCI interface and a CSP1040 silicon DAA. It supports V.92/V.90/V.34 data and FAX rates up to 14.4 kbits/s. The SV92PP is an interface device with the memory required for receiving, transmitting, and buffering data sent between the host system (soft modem stack) and the DAA or line codec.

- The SV92PP uses the PCI interface to communicate with the host system or soft modem stack. This interface has two bus mastering streams and 5 V tolerant buffers. With this interface, the SV92PP can take control of the local bus and exchange data directly with the host system memory.
- The CSP1040 has an integrated line codec and is part of the line interface. The CSP1040 sits on the high-voltage side of the modem circuit. It interfaces with the SV92PP via a digital isolation barrier.



## Integrated PCI Interface

The SV92PP incorporates a PCI interface that is compliant with PCI local bus specification revision 2.3. The interface also supports two full-duplex bus master streams for greater access to host system resources. One PCI stream is for control data, and the second full-duplex stream is used to transport data between the SV92PP and the host system memory. The interface supports both 3.3 V and 5 V signaling systems. However, the device supply must come from a 3.3 V PCI supply, Vaux, or a regulated 3.3 V supply.

## V.92 Features

The *ITU-T* recommendation for V.92 includes several features that are supported by LSI's soft modem drivers. Downstream connection rates of up to 56000 bits/s are supported when connecting to V.90/V.92 capable server modems through an appropriate path. Not all phone lines can support these connection rates. LSI's soft modem driver will automatically fall back to V.34 rates if a V.90/V.92 connection is not negotiated. When connecting in V.92 mode, the fast-connect feature allows the modem to shorten the start-up time on recognized connections. If call-waiting service is available, the modem-on-hold feature allows the user to place the modem connection on hold and switch to an incoming voice call if desired.

## Ordering Information

Table 1. SV92PP Chip Set Part Numbers

DEVICE	PART NUMBER	PACKAGE	COMCODE	VOLTAGE
L-SV92PP	L-SV92PP-M68-D	68-pin MLCC05	7000773770	3.3 V with 5 V tolerant buffers
	L-SV92PP-M68-DT	68-pin MLCC05	7000773780	3.3 V with 5 V tolerant buffers
L-CSP1040	L-CSP1040A3-E11-D	20-pin ETSSOP	7110072260	—
	L-CSP1040A3-E11-DT	20-pin ETSSOP	7110072670	—

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